

Contents

Preface.....	vii
Acknowledgements.....	xiii
Chapter 1 Fundamental Concepts in Fault Tolerance and Reliability Analysis.....	1
1.1 Introduction.....	1
1.2 Redundancy Techniques.....	4
1.2.1 Hardware Redundancy.....	5
1.2.1.1 Passive (Static) Hardware Redundancy.....	5
1.2.1.2 Active (Dynamic) Hardware Redundancy.....	6
1.2.1.3 Hybrid Hardware Redundancy.....	7
1.2.2 Software Redundancy.....	9
1.2.2.1 Static Software Redundancy Techniques.....	9
1.2.2.2 Dynamic Software Redundancy Techniques...	10
1.2.3 Information Redundancy.....	12
1.2.3.1 Error Detecting Codes.....	14
1.2.3.2 Error Correcting Codes.....	18
1.2.3.3 SEC-DED Codes.....	20
1.2.3.4 CRC Codes.....	26
1.2.3.5 Convolution Codes.....	27
1.2.4 Time Redundancy.....	29
1.2.4.1 Permanent Error Detection with Time Redundancy.....	30
1.3 Reliability Modeling and Evaluation.....	33
1.3.1 Empirical Models.....	34
1.3.2 The Analytical Technique.....	34
1.4 Summary.....	42
References.....	42

Chapter 2	Fault Modeling, Simulation and Diagnosis.....	44
2.1	Fault Modeling	44
2.2	Fault Simulation.....	51
2.3	Fault Simulation Algorithms	52
2.3.1	Serial Fault Simulation Algorithm.....	52
2.3.2	Parallel Fault Simulation.....	53
2.3.3	Deductive Fault Simulation.....	54
2.3.4	Concurrent Fault Simulation.....	57
2.3.5	Critical Path Tracing	57
2.4	Fault Diagnosis	59
2.4.1	Combinational Fault Diagnosis.....	59
2.4.2	Sequential Fault Diagnosis Methods.....	61
2.5	Summary	64
	References.....	64
Chapter 3	Error Control and Self-Checking Circuits.....	66
3.1	Error-Detecting/Error-Correcting Codes	67
3.2	Self-Checking Circuits.....	81
3.3	Summary	92
	References.....	92
Chapter 4	Fault Tolerance in Multiprocessor Systems	94
4.1	Fault Tolerance in Interconnection Networks.....	95
4.2	Reliability and Fault Tolerance in Single Loop Architectures ..	104
4.3	Introduction to Fault Tolerance in Hypercube Networks	108
4.4	Introduction to Fault Tolerance in Mesh Networks	120
4.5	Summary	125
	References.....	126
Chapter 5	Fault-Tolerant Routing in Multi-Computer Networks ...	127
5.1	Introduction.....	127
5.2	Fault-Tolerant Routing Algorithms in Hypercube.....	131
5.2.1	Depth-First Search Approach.....	131
5.2.2	Iterative-Based Heuristic Routing Algorithm	135

5.3	Routing in Faulty Mesh Networks	140
5.3.1	Node Labeling Technique	140
5.3.2	A FT Routing Scheme for Meshes with Non-Convex Faults	141
5.4	Algorithm Extensions	147
5.4.1	Multidimensional Meshes	147
5.4.2	Faults with f-Chains	148
5.5	Summary	149
	References	149
Chapter 6	Fault Tolerance and Reliability in Hierarchical Interconnection Networks	152
6.1	Introduction	152
6.2	Block-Shift Network (BSN)	154
6.2.1	BSN Edges Groups	155
6.2.2	BSN Construction	156
6.2.3	BSN Degree and Diameter	158
6.2.4	BSN Connectivity	158
6.2.5	BSN Fault Diameter	159
6.2.6	BSN Reliability	160
6.3	Hierarchical Cubic Network (HCN)	161
6.3.1	HCN Degree and Diameter	162
6.4	HINs versus HCNs	163
6.4.1	Topological Cost	163
6.5	The Hyper-Torus Network (HTN)	166
6.6	Summary	170
	References	170
Chapter 7	Fault Tolerance and Reliability of Computer Networks	172
7.1	Background Material	173
7.2	Fault Tolerance in Loop Networks	174
7.2.1	Reliability of Token-Ring Networks	175
7.2.2	Reliability of Bypass-Switch Networks	176

7.2.3	Double Loop Architectures	176
7.2.4	Multi-Drop Architectures	178
7.2.5	Daisy-Chain Architectures	178
7.3	Reliability of General Graph Networks	180
7.3.1	The Exact Method	180
7.3.2	Reliability Bounding	185
7.4	Topology Optimization of Networks Subject to Reliability & Fault Tolerance Constraints	188
7.4.1	Enumeration Techniques	189
7.4.1.1	Network Reliability	195
7.4.2	Iterative Techniques	199
7.5	Maximizing Network Reliability by Adding a Single Edge	204
7.6	Design for Networks Reliability	204
7.7	Summary	205
	References	206
Chapter 8	Fault Tolerance in High Speed Switching Networks	208
8.1	Introduction	208
8.2	Classification of Fault-Tolerant Switching Architectures	212
8.3	One-Fault Tolerance Switch Architectures	213
8.3.1	Extra-Stage Shuffle Exchange	213
8.3.2	Itoh Network	214
8.3.3	The B-Tree Network	215
8.3.4	Benes Network	216
8.3.5	Parallel Banyan Network	217
8.3.6	Tagle & Sharma Network	218
8.4	Two-Fault Tolerance Switch Architectures	219
8.4.1	Binary Tree Banyan Network	219
8.5	Logarithmic-Fault Tolerance	220
8.5.1	RAZAN	220
8.5.2	Logical Neighborhood	222
8.5.3	Improved Logical Neighborhood	223
8.6	Architecture-Dependent Fault Tolerance	224
8.7	Summary	226
	References	226

Chapter 9	Fault Tolerance in Distributed and Mobile Computing Systems	229
9.1	Introduction.....	229
9.2	Background Material	231
9.3	Checkpointing Techniques in Mobile Networks	236
9.3.1	Minimal Snapshot Collection Algorithm	237
9.3.2	Mutable Checkpoints	239
9.3.3	Adaptive Recovery.....	241
9.3.4	Message Logging Based Checkpoints.....	243
9.3.5	Hybrid Checkpoints	244
9.4	Comparison.....	245
9.5	Summary	247
	References.....	247
Chapter 10	Fault Tolerance in Mobile Networks	249
10.1	Background Material	249
10.2	More on Mutable Checkpoint Techniques in Mobile Networks	251
10.2.1	Handling Mobility, Disconnection and Reconnection of MHs	252
10.2.2	A Checkpointing Algorithm Based on Mutable Checkpoints.....	253
10.2.3	Performance Evaluation	261
10.3	Hardware Approach for Fault Tolerance in Mobile Networks ..	265
10.4	Summary	273
	References.....	273
Chapter 11	Reliability and Yield Enhancement of VLSI/WSI Circuits	276
11.1	Defect and Failure in VLSI Circuits	276
11.2	Yield and Defect Model in VLSI/WSI Circuits.....	279
11.3	Techniques to Improve Yield.....	284
11.4	Effect of Redundancy on Yield.....	286
11.5	Summary	288
	References.....	288

Chapter 12 Design of Fault-Tolerant Processor Arrays.....	291
12.1 Introduction.....	291
12.2 Hardware Redundancy Techniques	294
12.3 Self-Reconfiguration Techniques	317
12.4 Summary	321
References.....	322
 Chapter 13 Algorithm-Based Fault Tolerance.....	 326
13.1 Checksum-Based ABFT for Matrix Operations	327
13.2 Checksum-Based ABFT Error Handling	330
13.3 Weighted Checksum Based ABFT	331
13.4 ABFT on a Mesh Multiprocessor	332
13.5 Checksum-Based ABFT on a Hypercube Multiprocessor.....	334
13.6 Partition-Based ABFT for Floating-Point Matrix Operations ...	336
13.7 Summary	339
References.....	339
 Chapter 14 System Level Diagnosis-I.....	 341
14.1 Background Material and Basic Terminology	342
14.2 System-Level Diagnosis Models	347
14.3 Diagnosable Systems	352
14.4 Diagnose-Ability Algorithms.....	358
14.4.1 Centralized Diagnosis Systems	359
14.4.2 Distributed Diagnosis Systems	365
14.5 Summary	372
References.....	373
 Chapter 15 System Level Diagnosis-II.....	 378
15.1 Diagnosis Algorithms for Regular Structures.....	378
15.2 Regular Structures.....	379
15.3 Pessimistic One-Step Diagnosis Algorithms for Hypercube	380
15.4 Diagnosis for Symmetric Multiple Processor Architecture	383
15.5 Summary	394
References.....	394
Appendix.....	397

Chapter 16	Fault Tolerance and Reliability of the RAID Systems...	400
16.1	Introduction.....	401
16.2	Redundancy Mechanisms	403
16.3	Simple Reliability Analysis	411
16.4	Advanced RAID Systems	413
16.5	More on RAIDs	418
16.6	Summary	423
	References.....	423
Chapter 17	High Availability in Computer Systems	426
17.1	Introduction.....	426
17.2	Tandem High Availability Computers at a Glance.....	430
17.3	Availability in Client/Server Computing	438
17.4	Chapter Summary	440
	References.....	440